

AMENDMENTS TO THE SPECIFICATION:

Please amend the specification as follows:

Please replace the deleted paragraph from line 20 on page 24 to line 5 on page 25 by Examiner's amendment with the following paragraph:

Here, the mode discriminating section 3 is an example of a mode discriminating section for discriminating one of the operation modes at every operation cycle constituted by, as one unit, an operation period for carrying out the access operation and a stand-by period from an end of the operation period to a start of a next operation period, and an example of a mode discriminating circuit for discriminating between the data-input/output mode and the refresh mode at every operation cycle constituted by, as on unit, an operation period for carrying out the access operation and a stand-by period from an end of the operation period to a start of a next operation period. Besides, the switch section 6 is an example of a switching section for switching among the internal states, and an example of an address switching circuit for switching a connection to a decoding circuit at every output of the switching control signal, while one of an external address used in the data-input/output mode and a refresh address from a refresh address counter used in the refresh mode is always connected to the decoding circuit. Besides, the switch change-over signal SW is an example of a switching control signal outputted by a switch control section, the switch control signal is outputted to the switching section in accordance with a discrimination result obtained in the mode discriminating section, in which the switching control signal is not outputted in a stand-by period before a start of the operation cycle, but is outputted in the operation period

subsequent to the start of the operation cycle, and the switching control signal is outputted in the operation period after a start of the operation cycle only in a case where an operation mode discriminated by the mode discriminating circuit is different from an operation mode in the former operation cycle. Besides, the mode record holding section 4 and the comparator section 5 constitute a switching control section for outputting the switch control signal to the switching section in accordance with a discrimination result obtained in the mode discriminating section, and constitute a switching control circuit for outputting a switching control signal in the operation period after a start of the operation cycle only in a case where an operation mode discriminated by the mode discriminating circuit is different from an operation mode in the former operation cycle. Among these, the mode record holding section 4 is an example of a recording section that renews the switching control signal only in a case where the discrimination result in the mode discriminating section is different from the discrimination result in the former operation cycle.

Besides, S3 and S4 in the flowchart of Fig. 3 express an example of a mode discriminating process for discriminating one of the operation modes at every operation cycle constituted by, as one unit, an operation period for carrying out the access operation and a stand-by period from an end of the operation period to a start of a next operation period, and S5 to S8 express an example of a switch control process of renewing a switching control signal selecting one of the address supply paths in accordance with a discrimination result of the mode discriminating process, not in a

stand-by period before a start of the operation cycle, but in an operation period subsequent to the start of the operation cycle.

Please replace the deleted paragraph from line 4 on page 39 to line 14 on page 39 by Examiner's amendment with the following paragraph:

Here, the mode discriminating circuit 11 is an example of the mode discriminating section for discriminating one of the operation modes at every operation cycle constituted by, as one unit, an operation period for carrying out the access operation and a stand-by period from an end of the operation period to a start of a next operation period, and an example of the mode discriminating circuit for discriminating between the data-input/output mode and the refresh mode at every operation cycle constituted by, as one unit, an operation period for carrying out the access operation and a stand-by period from an end of the operation period to a start of a next operation period. Besides, the address switching circuit 13 is an example of the switching section for switching among the internal states, and an example of an address switching circuit for switching a connection to a decoding circuit at every output of the switching control signal, while one of an external address used in the data-input/output mode and a refresh address from a refresh address counter used in the refresh mode is always connected to the decoding circuit. Besides, the switch change-over signal SW is an example of the switching control signal outputted by a switch control section, the switch control signal is outputted to the switching section in accordance with a discrimination result obtained in the mode discriminating section, in which the switching control signal is not outputted in

a stand-by period before a start of the operation cycle, but is outputted in the operation period subsequent to the start of the operation cycle, and the switching control signal is outputted in the operation period after a start of the operation cycle only in a case where an operation mode discriminated by the mode discriminating circuit is different from an operation mode in the former operation cycle. Besides, the switch holding circuit 12 is the switching control section for outputting the switch control signal to the switching section in accordance with a discrimination result obtained in the mode discriminating section, and constitute a switching control circuit for outputting a switching control signal in the operation period after a start of the operation cycle only in a case where an operation mode discriminated by the mode discriminating circuit is different from an operation mode in the former operation cycle. Besides, it is an example of the recording section that renews the switching control signal only in a case where the discrimination result in the mode discriminating section is different from the discrimination result in the former operation cycle.

Please replace the deleted paragraph from line 21 on page 41 to line 6 on page 42 by Examiner's amendment with the following paragraph:

Here, the mode discriminating section 3 is an example of a mode discriminating section for discriminating one of the operation modes at every operation cycle constituted by, as one unit, an operation period for carrying out the access operation and a stand-by period from an end of the operation period to a start of a next operation period, and an example of a mode discriminating circuit for discriminating between the

data-input/output mode and the refresh mode at every operation cycle constituted by, as on unit, an operation period for carrying out the access operation and a stand-by period from an end of the operation period to a start of a next operation period. Besides, the switch section 6 is an example of a switching section for switching among the internal states. The switch change-over signal SW is an example of a switching control signal outputted by a switch control section, the switch control signal is outputted to the switching section in accordance with a discrimination result obtained in the mode discriminating section, in which the switching control signal is not outputted in a stand-by period before a start of the operation cycle, but is outputted in the operation period subsequent to the start of the operation cycle, and the switching control signal is outputted in the operation period after a start of the operation cycle only in a case where an operation mode discriminated by the mode discriminating circuit is different from an operation mode in the former operation cycle. Besides, the mode record holding section 4 and the comparator section 5 constitute a switching control section for outputting the switch control signal to the switching section in accordance with a discrimination result obtained in the mode discriminating section, and constitute a switching control circuit for outputting a switching control signal in the operation period after a start of the operation cycle only in a case where an operation mode discriminated by the mode discriminating circuit is different from an operation mode in the former operation cycle. Among these, the mode record holding section 4 is an example of a recording section that renews the switching control signal only in a case

where the discrimination result in the mode discriminating section is different from the discrimination result in the former operation cycle.

Besides, S3 and S4 in the flowchart of Fig. 10 are an example of a mode discriminating process for discriminating one of the operation modes at every operation cycle constituted by, as one unit, an operation period for carrying out the access operation and a stand-by period from an end of the operation period to a start of a next operation period, and S5, S6, S27 and S8 are an example of a switch control process of renewing a switching control signal selecting one of the address bit number of the activating sections in accordance with a discrimination result of the mode discriminating process, not in a stand-by period before a start of the operation cycle, but in an operation period subsequent to the start of the operation cycle.

Please replace the deleted paragraph from line 24 on page 48 to line 3 on page 49 by Examiner's amendment with the following paragraph:

Here, the mode discriminating circuit 11 is an example of the mode discriminating section for discriminating one of the operation modes at every operation cycle constituted by, as one unit, an operation period for carrying out the access operation and a stand-by period from an end of the operation period to a start of a next operation period, and an example of the mode discriminating circuit for discriminating between the data-input/output mode and the refresh mode at every operation cycle constituted by, as one unit, an operation period for carrying out the access operation and a stand-by period from an end of the operation period to a start of a next operation period. Besides, the

block decoding circuit 23 is an example of the switching section for switching among the internal states. The switch change-over signal SW is an example of the switching control signal outputted by a switch control section, the switch control signal is outputted to the switching section in accordance with a discrimination result obtained in the mode discriminating section, in which the switching control signal is not outputted in a stand-by period before a start of the operation cycle, but is outputted in the operation period subsequent to the start of the operation cycle, and the switching control signal is outputted in the operation period after a start of the operation cycle only in a case where an operation mode discriminated by the mode discriminating circuit is different from an operation mode in the former operation cycle. Besides, the switch holding circuit 12 is the switching control section for outputting the switch control signal to the switching section in accordance with a discrimination result obtained in the mode discriminating section, and constitute a switching control circuit for outputting a switching control signal in the operation period after a start of the operation cycle only in a case where an operation mode discriminated by the mode discriminating circuit is different from an operation mode in the former operation cycle. Besides, it is an example of the recording section that renews the switching control signal only in a case where the discrimination result in the mode discriminating section is different from the discrimination result in the former operation cycle.